1996 Paper 4 Question 6

Structured Hardware Design

Describe logic synthesis by writing short notes on *each* of the following topics:

(*a*) input language constructs

(*b*) output format

(*c*) use of libraries of subcircuits

(*d*) technology independence

(*e*) guiding metrics and constraints

(*f*) synthesis for multiple instances

(*g*) disadvantages compared with alternative approaches

[20 marks]